The impact of PV generation and load types on the impedance relays during both balanced and unbalanced faults

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Abstract- Photovoltaic (PV) power generation and the types of connected loads both have an effect on protective impedance relays’ readings. This paper investigates this effect in a real distribution system installed in the State of Kuwait. It is found that, both the dynamic loads and the PV plants have considerable effects in the relay impedance value which vary according to the load type, PV connection and fault location. Both single phase to ground fault (unsymmetrical fault) and three phase fault (symmetrical fault) are investigated. When single line to ground fault occurs at the PV bus (far from relay location), the dynamic loads increase the relay impedance while the PV plant decreases the relay impedance. When a single phase to ground fault occurs at the relay bus (load bus), the dynamic load decreases the relay impedance and the PV plant increases it. For a three-phase to ground fault at the relay bus, both dynamic load percentages and PV plant power generation have no effect on the protective relay impedance readings. At this condition, the relay impedance totally depends on the fault resistance. The main finding of this paper is that both the load type (especially dynamic load) and the PV plant have dominant effects on the protective impedance relay reading and setting. The distribution system planners and operators must consider the PV plant and types of load during designing, setting and adjusting the
protective impedance relays. The most important point in this paper is considering real case study. This means that, the obtained results are more realistic than the assumed system in the other research.

If the fault occurs at the location of the PV system’s bus when no PV power is generated, the dynamic load causes the relay impedance to increase, while connecting the PV decreases the relay impedance. The relay’s resistance and reactance increase from 0.3153Ω and 1.4950Ω, to 0.3456Ω and 1.6617Ω respectively when the dynamic load increases from 25% to 90% of the total load at constant high fault resistance. The relay resistance and reactance decrease from 0.2849Ω and 0.3443Ω (without PV plant), to 0.2195Ω and 0.3137Ω (with PV), respectively. When the dynamic load percentage increases from 25% to 90%, the resistance and reactance of the relay decrease from 1.0488Ω and 0.0051Ω, to 0.9526Ω and 0.0008Ω, respectively. This phenomenon is valid for all expected fault resistances. When considering constant dynamic load percentage and constant fault resistance, the relay resistance and reactance increase from 1.375Ω and 0.0022Ω (without PV) to 1.5745Ω and 0.0726 Ω (with PV), respectively. Based on those results, the impedance relay setting must be adjusted according to the percentage of the dynamic loads percentage, the PV penetration level, and the fault location.

**Keywords:** PV, static loads, dynamic loads, impedance relay, relay setting, fault resistance, symmetrical and unsymmetrical faults.

1. **Introduction**

For the past years, solar energy generation has increased significantly. The aim of PV integration is to reduce the carbon emissions by reducing the generated power from conventional fossil fuel power plants. Many studies are focusing on the impacts of PV distributed generation (PVDG) on the distribution side, which have various issues on the power system because of its interment nature. Some
of the complications caused by PVDG are harmonics, voltage and frequency variations and power imbalance. The issues related to PV penetration and its impacts on the power system are reviewed in [1] (Karimi et al., 2016) and [2] (Eltawil and Zhao, 2010).

Photovoltaic designs, operations and maintenances are reviewed in [3] (Hernández-Callejo et al., 2019). Monitoring Photovoltaic distributed generation (PVDG) is necessary because of the intermittent nature of PV where it requires real-time performance monitoring for system control and protection [4](Madeti and Singh, 2017a). PV power flow can be reversed where it can cause voltage rise unlike conventional power plants [5](Masters, 2002).

A more recent study distinguishes between voltage increasing and voltage decreasing of a power system with PV reverse flow which depends on the impedance of the line and the PV system power factor 6](Iioka et al., 2019). It is proposed by (Mortazavi et al., 2015) [7] to utilize impedance measurements of the distance relays for monitoring power system loading and PV system power penetration. The measured apparent impedance is the combination of both the load and line impedances.

Integrating DG to the conventional power grid can impact protection system coordination and therefore requires modified protection schemes (Darwish et al., 2013)[8]. The impact of high PV penetration is modelled and studies for a balanced three phase system in (Bracale et al., 2017) [9]. Interconnecting PVDG to the power system can cause changes in the apparent impedances and affect the function of distance relays [10](Sorrentino et al., 2018). The protection challenges are review in [11](Telukunta et al., 2017). A new approach to adjust protection relays considers fault current limiters with minimum number of relays to be adjusted [12](Ibrahim et al., 2017). A design procedure for over current relay with renewable generation is proposed in [13](Chen, 2017). False detection caused by distribution of non-linear loads is studied in [14](Soheili et al., 2018). The effect of Static Synchronous Series Compensator (SSSC) on distance relay calculation is investigated in [15](Ghorbani et al., 2012).
Different types of fault detections for both ac and dc sides of the PV power systems are reviewed in [16](Madeti and Singh, 2017b).

A study in (Kim and Aggarwal, 2006) [17] suggests utilizing the distance relay for monitoring the transmission line operating condition. An online-measurement procedure for improving the distance relay accuracy is proposed which can be used during normal or fault conditions. The protection relay setting depends on the accurate phasor measurements of the voltage and current.

Up to the author’s knowledge, limited published work deals with the impact of load types on the impedance relay reading combined with DG. Also, the effects of DGs especially the PV on the protective relay readings and settings are not investigated in detail where the published work mostly assumed distribution system loads as constant power static loads and did not consider other load types such as rotating loads (dynamic loads) combined with PVDG.

In latest work related to this topic, a study by (Mishra et al., 2021) considers a 39-bus system with a 300 MW renewable plant modelled in PSCAD. The study investigates a large PV plant and at one location in the power system. The authors proposed an adoptive relay technique that measures the line impedance up to the fault point and obtains a deviation angle to adjust the relay setting.

The main goal of this study is a detailed investigation and analysis of the effects of load types with PVDG on the impedance relay reading and setting. This paper considers both rotating and static load types. In addition, the faults are simulated at different location (at PV bus or at load bus) and their effects on the relay reading and setting are studied in detail. The remaining of the paper is organized as follows:

Section 2 describes the real distribution system with installed PV (10 MWp) and real loads. Section 3 deals with a mathematical analysis and description of different types of load. Also, the influence of the PV on the fault current of the distribution system has been described in section 3. Section 4 analyzes the performance of the impedance protective relay under symmetrical and unsymmetrical faults. Section 5 displays the obtained results with different load types and with or without PV under both symmetrical and unsymmetrical faults and reports the main finding of paper. Conclusions are reported in section 6.
2. Methodology

Architecture of the investigated distribution network with PV and loads

2.2.1 Flow chart

In this section, we summarize the methods used for investigating the effect of both PV and load type effect on the impedance relay. We generalize the procedure into the chart in Fig.1 so it can be a useful tool for other researcher and for future studies. The first step is to model the power systems involved in the study including the distribution system and the solar power system. Then, we model the overall power system in ETAP software in order to perform the short circuit analysis in step 4. different scenarios are under investigation to study the effect of fault location, fault impedance, fault type, and load type, on the obtained results.

10MWp grid connected PV plant to one of the Kuwait Oil Company’s (KOC) electrical distribution system which supplies electricity to oil pump at the Umm-Gudair field, West Kuwait (Fig. 1.a.) [18]. The distribution system which contains the PV plant is shown in Fig.1.b. The main grid feeding the load bus (KOC bus) is the “Minagish B” substation. Four feeders (incomers) coming from the “Minagish B” substation (11 kV) and represent bus 1 (Fig.1.b). Three of the four incomers are continuous, and one
is spare for redundancy. The four incomers are operating with closed breakers. The 11 kV load bus is Bus2 (Fig.1.b). The PV plant is installed at Bus 3. The distances between different buses are shown in Fig. 1(1.b) [18].

2.2 Architecture of the investigated distribution network with PV and loads

A 10MWp grid connected PV plant to one of the Kuwait Oil Company’s (KOC) electrical distribution system which supplies electricity to oil pump at the Umm-Gudair field, West Kuwait (Abdullah et al., 2016). The distribution system which contains the PV plant is shown in Fig.2.b. The main grid feeding the load bus (KOC bus) is the “Minagish B” substation. Four feeders (incomers) coming from the “Minagish B” substation (11 kV) and represent bus 1 (Fig.2.b). Three of the four incomers are continuous, and one is spare for redundancy. The four incomers are operating with closed breakers. The 11 kV load bus is Bus2 (Fig.2). The PV plant is installed at Bus 3. The distances between different buses are shown in Fig.2 (Abdullah et al., 2016).

4.3 Mathematical analysis and modelling of different loads and PV interfacing inverter

4.3.1 Different load types

There are two main types of the electrical loads, static and dynamic (rotating) loads. The description and mathematical modelling of the two load types are described in the following subsections.

4.4.4.1 Static load modelling

A static load model is the characteristics of the load as a function of the voltage and frequency (IEEE Task Force on Load Representation for Dynamic Performance, 1993), (Abdullah et al., 2016), and (Kamel and Nagasaka, 2015)[19-21]. The static load real power (P) and imaginary power (Q)
components are considered separately. The dependency of real power \( P \) and imaginary power \( Q \) on the magnitude of the voltage \( V \) can be calculated as follows (Kamel and Nagasaka, 2015) [21]:-

\[
P = P_o (V/V_o)^a, \quad (1)
\]

\[
Q = Q_o (V/V_o)^b, \quad (2)
\]

\[
V = \frac{V}{V_o}, \quad (3)
\]

where \( P_o, Q_o, V_o \) represent initial conditions of the real, imaginary powers and voltage, respectively. The \( a \) and \( b \) are the parameters of the load model. Three models are available as follows [20-21](Kundur, 1994), and (Kamel and Nagasaka, 2015):

a) Static load with constant power when the parameters \( a = b = 0 \).

b) Static load with constant impedance when the parameters \( a = b = 1 \).

c) Static load with a constant current when \( a = b = 2 \).

d) Fig1.a: 10 MWp grid connected PV located west Kuwait
The voltage dependency of load model is given by the following equation:

\[
P = P_o \left[ p_1 \bar{V}^2 + p_2 \bar{V} + p_3 \right],
\]

(4)

\[
Q = Q_o \left[ q_1 \bar{V}^2 + q_2 \bar{V} + q_3 \right]
\]

(5)

The effect of frequency on the load characteristics is given by the following equation:

\[
P = P_o \left( \bar{V} \right)^6 \left( 1 + k_P \Delta f \right),
\]

(6)

\[
Q = Q_o \left( \bar{V} \right)^8 \left( 1 + k_Q \Delta f \right),
\]

(7)

or

\[
P = P_o \left[ p_1 \bar{V}^2 + p_2 \bar{V} + p_3 \right] \left( 1 + k_P \Delta f \right),
\]

(8)
\[ Q = Q_u [q_1 \bar{V} + q_2 \bar{V} + q_3] (1 + k_{qf} \Delta f) \], \quad (9)

where, \( \Delta f \) represents the frequency deviation \((f - f_c)\). Typically, \( k_{pf} \) is between 0 to 3.0 and \( k_{qf} \) is between 0 to -2.0 (Concordia and Ihara, 1982)[22].

A general model for static load is given as follows (Kamel and Nagasaka, 2015), [21], and [23](Kundur et al., 1993):

\[ P = P_u [P_{ZIP} + P_{EX1} + P_{EX2}] \], \quad (10)

where,

\[ P_{ZIP} = p_1 \bar{V}^2 + p_2 \bar{V} + p_3 \], \quad (11)

\[ P_{EX1} = p_4 (\bar{V})^{q1} (1 + k_{qf1} \Delta f) \], \quad (12)

\[ P_{EX2} = p_5 (\bar{V})^{q2} (1 + k_{qf2} \Delta f) \], \quad (13)

\[ Q = Q_u [Q_{ZIP} + Q_{EX1} + Q_{EX2}] \], \quad (14)

\[ Q_{ZIP} = q_1 \bar{V}^2 + q_2 \bar{V} + q_3 \], \quad (15)

\[ Q_{EX1} = q_4 (\bar{V})^{q1} (1 + k_{qf1} \Delta f) \], \quad (16)

\[ Q_{EX2} = q_5 (\bar{V})^{q2} (1 + k_{qf2} \Delta f) \] \quad (17)

### 4.1.2.3.1.2 Modelling of dynamic loads

Induction (asynchronous) motors represent the widely used dynamic loads. Typically, asynchronous motors dissipated 50\% to 70 \% of the total power system energy [23](Kundur et al., 1993). Fig.23 shows the asynchronous machine equivalent circuit referred to the machine stator side.

![Equivalent circuit referred to stator of the asynchronous machine](image)

Fig.32: Equivalent circuit referred to stator of the asynchronous machine
In Fig. 32, \( S \) is the slip speed of the machine. The typical value of asynchronous machine running slip ranges between 0 to 3.0% (Kamel and Nagasaka, 2015)[21], and (Kamel, 2014) [24].

The initial value of the injected fault current by the induction machine (rotating load) is approximately equal to the machine locked rotor current. For the balanced fault (three phase fault), the mathematical equation which estimates the contribution of the asynchronous machine is given as follows (Kamel and Nagasaka, 2015) [21], and (Morren and de Haan, 2007)[25]:

\[
i(t) = \frac{\sqrt{2} V_s}{X_S} \left[ e^{-\frac{t}{\tau_S}} \sin(\alpha) - (1 - \sigma) e^{-\frac{t}{\tau_r}} e^{j\omega t} \sin(\omega t + \alpha) \right],
\]

where,

\( \alpha \) is the voltage phase angle at the instant of fault occurrence.

\( \sigma \) is the flux leakage factor,

\( X_t = \omega L_s \) is the transient reactance of asynchronous machine stator.

\( \tau_S \) and \( \tau_r \) are the machine stator and rotor time constants, and

\( \omega \) is frequency in radians per second.

The transient reactance \( \bar{X}_S \) and \( \bar{X}_r \) of the machine stator and rotor can be evaluated as (Kamel and Nagasaka, 2015), (Morren and de Haan, 2007), and (Sulawa et al., 2007):[21], [25], and [26]:

\[
\bar{X}_S = X_{so} + \frac{X_{sr} X_m}{X_{sr} + X_m},
\]

(19)

\[
\bar{X}_r = X_{ro} + \frac{X_{ro} X_m}{X_{so} + X_m}
\]

(20)

The transient time constants for stator and rotor and leakage factors are given by (Kamel, 2014) [24]:

\[
\tau_S = \frac{L_s}{R_s},
\]

(21)

\[
\tau_r = \frac{L_r}{R_r},
\]

(22)

\[
\sigma = 1 - \frac{L_s}{L_s L_r}
\]

(23)
\[ L_s = L_m + L_n \quad \text{and} \quad L_r = L_m + L_n \]  
(24)

According to (Kamel and Nagasaka, 2015)[21], the asynchronous machine peak fault current is estimated as:

\[ i_{s,\text{max}} = \frac{\sqrt{2}V_s}{X_s} \left[ e^{-\frac{T}{2\pi f}} + (1-\sigma)e^{-\frac{T}{2\pi f}} \right], \]  
(25)

Where \( T \) is the period time and is equal to reciprocal of \( f \) and \( f \) is the frequency of the grid (50 Hz).

From equation (25), the parameters of the machine are the dominant factors effecting the injected fault current by the dynamic load.

### 4.3.2 PV interfacing inverter model

Fig.2 (1.b) shows the configuration of the investigated distribution network. As shown, the PV bus (bus 3) is connected to the network through inverters. During a fault event, the interfacing inverter behaves totally different than the synchronous machine in the classical power grid. The synchronous machine feeds very high fault current, near 500% to 700% of its rated (full load) current (Kamel, 2014)[24]. On the other hand, the interfacing inverter contains a current limiter to protect the inverter switches from the high fault current (Morren and de Haan, 2007) [25]. The fault current of an inverter interfaced PV ranges from 110% to 200% of the rated current (Boutsika and Papathanassiou, 2008)[27]. In this paper, the PV inverters are represented by variable virtual impedances, which limit the fault currents to 200% of the inverters’ rated current (Abdel-Salam et al., 2012) [28].

### 5.4 Protective impedance relay performance during both balanced and unbalanced faults
The most important protective relay in the power system is the distance relay. The distance relay measures the impedance by dividing the measured voltage over the measured current at the relay place and is subsequently called impedance relay.

5.14.1 Principles of the protective impedance relay

Fig.45 shows the principle of operation of the protective impedance relay. The relay divides the relay measured voltage ($V_{Relay}$) by the relay measured current ($I_{Relay}$) (Fig.43). During normal operation (Fig.54.a), the relay impedance is very high ($Z_{Relay} = Z_L + Z_{Load}$). During a fault event (Fig.54.b), the load impedance is short circuited and the relay measures very low impedance.

5.24.2 Protective impedance relay reading during all fault types

In the following subsections, the readings of the protective impedance relay during symmetrical (three phase) fault and unsymmetrical (single line to ground, double lines to ground and line to line) faults will be analyzed.

4.2.1 Impedance relay reading under phase faults neglecting fault resistance ($R_f = 0$)

The phase faults include the three phase and the phase to phase faults. During those fault types, the earth is not a part of the circuit. The reading of the impedance relay is calculated by dividing the two faulty phase voltages over the two faulty phase current (Tleis, 2019), (Anderson, 1995), (Xu et al., 2010), and (Tseng et al., 2003) [29-32]. During phase to phase fault, and three line to ground fault, the voltage and current can be estimated as in table 1 (Tleis, 2019)[29].

Table 1: voltages and currents during line to line and three line to ground faults (Tleis, 2019) [29]

<table>
<thead>
<tr>
<th>Fault Quantity</th>
<th>Three lines to ground fault (A-B-C)</th>
<th>Line to Line fault (B-C)</th>
</tr>
</thead>
</table>

12
<table>
<thead>
<tr>
<th>$I_A$</th>
<th>$I^+$</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>$I_B$</td>
<td>$a^2I^+$, $a^2=1\angle240$</td>
<td>$(a^2-a)I^+$, $a=1\angle120$</td>
</tr>
<tr>
<td>$I_C$</td>
<td>$aI^+$</td>
<td>$(a-a^2)I^+$</td>
</tr>
<tr>
<td>$V_A$</td>
<td>$Z_L^+I^+$</td>
<td>$2(Z_L^++Z_L^+)I^+$</td>
</tr>
<tr>
<td>$V_B$</td>
<td>$a^2Z_L^+I^+$</td>
<td>$(2a^2Z_L^+-Z_S^+)I^+$</td>
</tr>
<tr>
<td>$V_C$</td>
<td>$aZ_L^+I^+$</td>
<td>$(2aZ_L^+-Z_S^+)I^+$</td>
</tr>
</tbody>
</table>

**Fig.43:** Principles and location of the impedance relay

**Fig.54.a:** Protective impedance relay during normal conditions

**Fig.54.b:** Protective impedance relay during fault
4.2.1.1 Protective impedance relay reading during line to line faults (phase B to phase C)

The relay reading is calculated as follows [29](Tleis, 2019):

\[
Z_{\text{Relay}} = \frac{V_{BC}}{I_{BC}}
\]

(26)

From table 1, \( Z_{\text{Relay}} \) is equal to:

\[
Z_{\text{Relay}} = \frac{V_{BC}}{I_{BC}} = \frac{V_B - V_C}{I_B - I_C} = \frac{2(a^2 - a)Z_{\text{LF}}^* I^*}{(a^2 - a) I^*} = Z_{\text{LF}}^*
\]

(27)

4.2.1.2 Protective impedance relay reading during three line to ground faults (A-B-C)

The relay reading is calculated as:

\[
Z_{\text{Relay}} = \frac{V_{AB}}{I_{AB}}
\]

(28)

From table 1, \( Z_{\text{Relay}} \) is equal to:

\[
Z_{\text{Relay}} = \frac{V_{AB}}{I_{AB}} = \frac{V_A - V_B}{I_A - I_B} = \frac{Z_{\text{LF}}^* I^* - a^2Z_{\text{LF}}^* I^*}{I^* - a^2I^*} = Z_{\text{LF}}^*
\]

(29)

From equation (27-29), the protective impedance relay reading is equal to the positive sequence line impedance from the relay position until the short circuit location.

4.2.2 Protective impedance relay reading under phase faults considering the resistance of the fault \( (R_f \neq 0) \)

Fig.56.a, and Fig.65.b show three lines and line to line short circuit considering the resistance of the fault. The relay reading in existence of fault resistance can be calculated as (Nikolaidis et al., 2018)[33]:

\[
V_C - V_B = Z_C I_C + R_f I_C
\]

\[
V_A - V_B = Z_A I_A + R_f I_A
\]

\[
V_B - V_C = Z_B I_B + R_f I_B
\]

\[
I_A = I_B + I_C
\]
Fig. 65.a: Three phase fault with $R_F$

![Diagram of three phase fault with $R_F$]

Fig. 65.b: Line to line short circuit considering $R_F$

4.2.2.1 Line to line fault (B-C) considering the resistance of the fault ($R_F \neq 0$)

The relay impedance during phase to phase fault in existence of fault resistance can be estimated by (Tleis, 2019)[29]:

$$Z_{\text{Relay}} = \frac{V_{BC}}{I_{BC}} = Z_{IF} + \frac{R_F}{2}$$  \hspace{1cm} (30)

4.2.2.2 Three line to ground fault (A-B-C) ($R_F \neq 0$)

The relay impedance during three phase (balanced) fault in existence of the $R_F$ in series with each phase (Fig.56.a) can be estimated by (Tleis, 2019)[29]:

$$Z_{\text{Relay}} = \frac{V_{AB}}{I_{AB}} = Z_{IF} + R_F$$  \hspace{1cm} (31)

4.2.3 Protective impedance relay reading during earth fault

The earth fault includes single line to ground and double lines to ground faults. During those two faults, the earth is apart from the circuit. Single phase to ground faults are the most common faults in the power system especially the distribution system. In the other side, double phase to ground faults rarely occur. Based on this fact, this paper investigates and highlights single phase to ground faults.

4.2.3.1 Single line to ground fault neglecting fault resistance ($R_F = 0$)
Fig. 6.7 represent schematic diagram of the single line to ground fault neglecting the fault resistance ($R_f = 0$). The relay impedance can be calculated as follows (Tleis, 2019)[29]:

$$Z_{Relay} = \frac{V_L}{I_A} = Z^+_{LF}$$  \hspace{1cm} (32)

![Schematic Diagram](image)

**Fig. 6.7:** Solid ground single line to ground fault ($R_f = 0$)

### 4.2.3.2 Single line to ground fault considering $R_f$ ($R_f \neq 0$)

Fig. 7.8.a shows single line to ground fault in existence of $R_f$. Fig. 7.8.b indicates its equivalent circuit.

The impedance relay reading can be calculated by (Tleis, 2019) [29]:

![Schematic Diagram](image)

**Fig. 7.8.a:** Single line to ground fault with fault resistance

![Schematic Diagram](image)

**Fig. 7.8.b:** Equivalent circuit of single line to ground fault considering fault resistance
The faulty phase (phase A) voltage is given by (Tleis, 2019)[29]:

\[ V_A = Z^{+}_{LF}I^+ + Z^{-}_{LF}I^- + Z^0_{LF}I^0 \]  

(33)

The short circuit current is given by:

\[ I_A = I^+ + I^- + I^0, \]  

(34)

\[ I^+ = I^- = I^0, \]  

(35)

\[ I_N = I_A + I_B + I_C = I_A = 3I^0 \]  

(36)

It is known that \( Z^{+}_{LF} = Z^{-}_{LF} \). Defining constant \( K \) equal to:

\[ K = \frac{Z^0_{LF}}{Z^+_{LF}} \]  

(37)

From equations (33), (34), (36), and (37), the faulty phase voltage can be estimated as:

\[ V_A = Z^{+}_{LF}(I_A + (I_A + I_B + I_C) \ast \frac{(K - 1)}{3} \]  

(38)

The impedance relay reading can be calculated by dividing \( V_A \) over \( I_A \) as follow (Tleis, 2019)[29]:

\[ Z_{Relay} = \frac{V_A}{I_A} = (1 + \frac{K - 1}{3})Z^{+}_{LF} \]  

(39)

6.5. Results and discussions

To indicate the effect of both load type and PVDG on the protective impedance relay reading and setting, the investigated distribution network in (Fig.12) is exposed to a single line to ground fault (the most common type in the distribution system) and three-lines to ground fault (the most severe fault), at the PV bus (bus3) then at the load bus (bus2). Four different main cases and 120 subcases are studied as follows:

1) Main case (1) includes 30 subcases, single line to ground fault at PV bus (bus3) with and without PV, different percentages of dynamic load, and different values of \( R_F \). The main case (1) and its subcases are indicated in table (2)
2) Main case (2) likes main case (1) except that three-line to ground fault instead of single line to ground fault occur at PV bus (bus3).

3) Main case (3) is like main case (1) except that the fault location is at load bus (bus2) instead of PV bus (bus3).

4) Main case (4) is like main case (2) except that the fault location is at load bus (bus2) instead of PV bus (bus3).

The following results indicate the effects of load types (for all cases) and PVDG on the protective impedance relay reading and setting at all main studied cases and subcases.

Table 2: Studied main case (1) and its subcases

<table>
<thead>
<tr>
<th>Fault resistance</th>
<th>Single line to ground fault at PV bus (bus 3)</th>
</tr>
</thead>
<tbody>
<tr>
<td>$R_f = 0 \text{ ohm}$</td>
<td>Without PV</td>
</tr>
<tr>
<td></td>
<td>25 %</td>
</tr>
<tr>
<td></td>
<td>dynamic</td>
</tr>
<tr>
<td></td>
<td>load</td>
</tr>
<tr>
<td>$R_f = 0.17 \text{ ohm}$</td>
<td>Without PV</td>
</tr>
<tr>
<td></td>
<td>25 %</td>
</tr>
<tr>
<td></td>
<td>dynamic</td>
</tr>
<tr>
<td></td>
<td>load</td>
</tr>
<tr>
<td>$R_f = 0.3 \text{ ohm}$</td>
<td>Without PV</td>
</tr>
<tr>
<td></td>
<td>25 %</td>
</tr>
<tr>
<td></td>
<td>dynamic</td>
</tr>
<tr>
<td></td>
<td>load</td>
</tr>
<tr>
<td>$R_f$ =0.9ohm</td>
<td>Without PV</td>
</tr>
<tr>
<td>-----------</td>
<td>-------------</td>
</tr>
<tr>
<td>25 %</td>
<td>50 %</td>
</tr>
<tr>
<td>dynamic</td>
<td>dynamic</td>
</tr>
<tr>
<td>load</td>
<td>load</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>$R_f$ =1.3ohm</th>
<th>Without PV</th>
<th>With PV</th>
</tr>
</thead>
<tbody>
<tr>
<td>25 %</td>
<td>50 %</td>
<td>90 %</td>
</tr>
<tr>
<td>dynamic</td>
<td>dynamic</td>
<td>dynamic</td>
</tr>
<tr>
<td>load</td>
<td>load</td>
<td>load</td>
</tr>
</tbody>
</table>

### 6.4.5.1 Single line to ground fault at PV bus under different dynamic load percentages with and without PV (different $R_f$)

![Relay impedance for different percentage dynamic loads](image)

Fig.98: Relay impedance at different dynamic load percentages without PV

Fig.89 shows the protective impedance relay resistance and reactance for different percentages of dynamic loads (90 %, 50%, and 25 %) without the PV plant. As shown, the relay impedance (especially reactance) decreases with decreasing of the dynamic load percentage. For example, at $R_f$ =1.3 Ω, if the dynamic load percentage decreases from 90% to 25%, the relay reactance and resistance decrease from 0.3456Ω and 1.6617Ω to 0.3153Ω and 1.4950Ω, respectively. This can be interpreted as follow: Increasing the dynamic load percentage will increase the fault current $I_{23}$.
(Fig.12.b), and consequently increase the voltage at bus2 (relay location). Consequently, the relay impedance increases. This phenomenon is correct for all values of $R_f$. When the dynamic load percentage decreases from 90% to 25%, for $R_f=0$ (solid grounded fault), the relay reactance and resistance decrease from 0.3443Ω and 0.2849Ω to 0.3114Ω and 0.2557 Ω, respectively.

![ Relay impedance for different percentage dynamic loads ]

Fig.910: Relay resistance and reactance for different dynamic load percentages with PV

Fig.910 shows the relay resistance and reactance for different dynamic load percentages while considering the PV plant fault current. Also, the relay impedance (both resistance and reactance) increase with increasing the dynamic load percentage in existence of PV plant. For $R_f=0$ Ω, the impedance relay resistance and reactance increase from 0.1877Ω and 0.2769Ω to 0.219 Ω and 0.3137Ω, respectively. This phenomenon is valid only for small value of the $R_f$ ($R_f=0, 0.17, 0.3, $ and 0.9 Ω) as shown in Fig.910. As the $R_f$ increases ($R_f=1.3$ Ω), the dynamic load percentage has a negligible effect on the relay impedance.
Fig.110: Relay impedance with different dynamic load percentages and different $R_F$ with and without PV for SLG fault at PV bus (bus3).

Fig.101 combines the results of both Fig.98 (without PV) and Fig.910 (with PV). As shown, existence of PV reduces the impedance (resistance and reactance) of the relay. The injected fault current by the interfacing inverter of the PV plant increases the total fault current. Consequently, the relay impedance decreases ($Z_{Relay} = \frac{V_{Z}}{I_{23}}$). For example, for 90% dynamic loads and $R_f = 0$ Ω, the relay resistance and reactance decreases from 0.2849 Ω and 0.3443 Ω (without PV plant) to 0.2195 Ω and 0.3137 Ω (with PV), as shown in Fig.110. This phenomenon is valid for small value of $R_F$. As the $R_F$ increases, the injected fault current by the PV plant has small effect in the relay reading.

Fig.11: Relay impedance with different dynamic load percentage and constant $R_F$ with and without PV.
Fig. 12: Relay impedance with different dynamic load percentages and different $R_f$ ($R_f = 0$ to $R_f = 1.3$ ohm) with and without PV for SLG fault at PV bus (bus3).

Fig. 11 and Fig. 12 display the effects of dynamic load percentage on the impedance relay resistance and reactance at constant $R_f$ without and with considering the PV plant. As shown, the relay impedance increases with the increase of dynamic load percentages for the same fault resistance with and without considering the PV plant. For high $R_f$, the existence of the PV plant has a small effect on the relay reading.

6.25.2 Three line to ground fault at PV bus for different dynamic loads percentages with and without PV (different values of $R_f$)
Fig.133: Impedance relay reading for 3-phase fault at PV bus for different percentages of dynamic load and $R_f$ with and without PV

Fig.133 shows the relay reading for three line to ground fault at bus 3 with and without PV under different percentage of dynamic load and different values of $R_f$. As shown, the dynamic load increases the resistance and reactance of the protective impedance relay. The injected fault current by the dynamic load flows through the line from bus 2 to bus 3 and flows through the fault resistance ($R_f$). This increases the voltage at the relay location (bus2), and consequently increases the relay impedance as shown in Fig.133. Without existence of the PV plant, the relay impedance for 90% dynamic load (solid blue line) is larger than its value for 25% dynamic load (solid black line).

With considering the PV plant, the injected fault current by the PV plant flows through the fault resistance and increases the voltage at the relay location (bus2). The relay impedance increases with the existence of the PV plant. For 90% dynamic load and fault resistance $R_f = 1.3\Omega$, the relay resistance and reactance increase from $3.0259 \Omega$ and $0.4452 \Omega$ (without PV) to $3.1877 \Omega$ and $0.6822 \Omega$ (with PV), respectively.

![Fig.154: Relay impedance for 3-line to ground fault at PV bus (bus 3) for constant $R_f$](image)

Fig.154 shows the effect of fault resistance on the relay impedance at different dynamic loads with and without the PV plant. As shown, for a high fault resistance ($R_f$), the PV plant has dominant effect.
on the protective relay impedance (Fig.144, violet lines solid and dashed). For small value of $R_F$, both the percentage of dynamic load and the PV plant has a negligible effect in the protective relay reading (Blue lines in Fig.144).

### 6.35.3 Single line to ground fault at load bus for different dynamic load percentages with and without PV (different $R_F$)

![Graph showing impedance relay reading for different dynamic load percentages with and without PV](image)

Fig.155: Single line to ground fault at load bus for different dynamic load percentage without PV

Fig.155 shows the impedance relay reading during single line to ground fault at load bus (bus2) without PV and for different dynamic load percentages. As shown, unlike the obtained results for single line to ground fault at PV bus (Fig.89), the relay impedance decreases with increasing the dynamic load percentage. When the fault is at load bus (bus2), the increase in the dynamic load percentage increases the fault current only and does not increase the relay voltage ($V_f$). Consequently, the relay impedance decreases with the increasingly dynamic load percentage. For example, when the dynamic load percentage increases from 25% to 90%, the resistance and reactance of the relay decreases from $1.0488\Omega$ and $0.0051\Omega$ to $0.9526\Omega$ and $0.0008\Omega$, respectively. This phenomenon is valid for all values of $R_F$.

Fig.16 shows the relay resistance and reactance for single phase to ground fault at load bus, different dynamic loads with considering the PV plant. The increasing in the dynamic load percentage decreases the relay impedance in existence of the PV plant.
Fig. 17 combines both Fig. 155 and Fig. 166 to investigate the effect of the PV plant on the impedance relay reading during single line to ground fault occurrence far from the PV plant bus (load bus). As shown in Fig. 12.b, when the fault occurs at the load bus (bus 2), the PV injects fault current ($I_{32}$). This current flows in the line between PV bus (bus 3) and fault bus (bus 2) causing an improvement to the load bus voltage ($V_2$) which increases the relay impedance. For example, at 90% dynamic load (blue lines in Fig. 177) and $R_F = 1.3\ \Omega$, the relay resistance and reactance increase from 1.375\(\Omega\) and 0.0022\(\Omega\) (without PV) to 1.5745\(\Omega\) and 0.0726\(\Omega\) (with PV), respectively.

![Diagram showing relay resistance and reactance for different dynamic loads with PV](image1)

Fig. 1616: Single line to ground fault at load bus for different dynamic loads with PV
Fig. 17: Single line to ground fault at load bus for different percentages of dynamic load and different $R_F$ with and without PV.

Fig. 18: Single line to ground fault at load bus with different percentages of dynamic loads, and constant $R_F$ ($R_F = 1.3 \, \Omega$), with and without PV.

Fig. 18 indicates the effect of both dynamic load percentages and PV plant on the relay reading for constant $R_F$ ($R_F = 1.3 \, \Omega$). The PV plant has a dominant effect on the protective relay reading (both relay resistance and relay reactance) when the fault resistance is held constant. The dynamic load percentage has a considerable effect only on the relay resistance, and a negligible effect on the relay reactance.

6.4.5.4 Three line to ground fault at load bus with different dynamic load percentages with and without PV (different $R_F$)
Fig. 199: Relay impedance for 3-phase fault at load bus (all cases)

Fig. 1919 displays the relay resistance and relay reactance for three line to ground fault at load bus (relay location). As shown, for all cases, the relay reactance equal to zero. The relay has resistance only which depends on the fault resistance ($R_F$) where $V_z = I_s R_F$. For three-phase fault at the relay bus, both the dynamic load percentage and the PV plant have no effect on the relay reading. With other words, the relay reading is only dependent on the fault resistance ($R_F$) for three phase symmetrical fault at its location.
8.6. Conclusions

This paper investigates in detail the effects of both dynamic load percentages and PV plant on the reading and setting of the protective impedance relay. Both balanced (three lines to ground) and unbalanced (single line to ground) faults at different buses (PV bus and load bus) are analyzed. If the fault occurs at the PV system’s bus when no PV power is generated (during night), the dynamic load increases the relay impedance, while connecting the PV decreases the relay impedance. The resistance and reactance of the relay increase from $0.3153\,\Omega$ and $1.4950\,\Omega$, to $0.3456\,\Omega$ and $1.6617\,\Omega$ respectively when the dynamic load increases from 25% to 90% of the total load. The resistance and reactance of the relay decrease from $0.2849\,\Omega$ and $0.3443\,\Omega$ (without PV plant), to $0.2195\,\Omega$ and $0.3137\,\Omega$ (with PV), respectively. When the dynamic load percentage increases from 25% to 90%, the resistance and reactance of the relay decrease from $1.0488\,\Omega$ and $0.0051\,\Omega$, to $0.9526\,\Omega$ and $0.0008\,\Omega$, respectively. This phenomenon is valid for all expected fault resistances. When considering constant dynamic load percentage and constant fault resistance, the relay resistance and reactance increase from $1.375\,\Omega$ and $0.0022\,\Omega$ (without PV) to $1.5745\,\Omega$ and $0.0726\,\Omega$ (with PV), respectively. Based on those results, the impedance relay setting must be adjusted according to the percentage of the dynamic loads, the PV penetration level, and the fault location. When single line to ground fault occurs at the PV bus (far from relay location), the dynamic loads increase the relay impedance while the PV plant decreases the relay impedance. When a single phase to ground fault occurs at the relay bus (load bus), the dynamic load decreases the relay impedance and the PV plant increases it. For a three-phase to ground fault at the relay bus, both dynamic load percentages and PV plant power generation have no effect on the protective relay impedance readings. At this condition, the relay impedance totally depends on the fault resistance. The main finding conclusion of this paper is that both the load type (especially dynamic load) and the PV plant have dominant considerable effects on the protective impedance relay reading and setting. The distribution system planners and operators must consider the PV plant and types of load during designing, setting and adjusting the protective impedance relays.
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